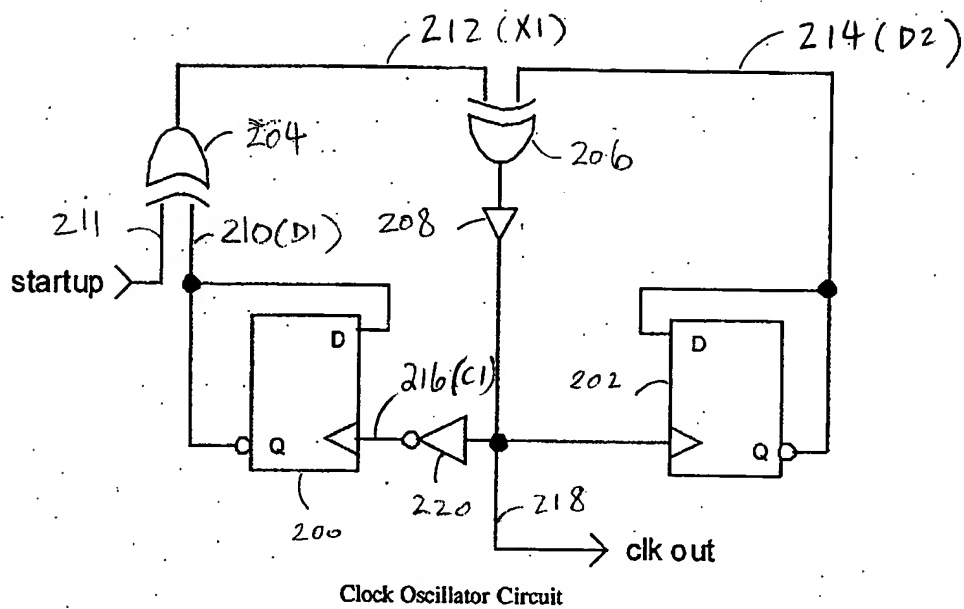


Figure 1



Clock Oscillator Circuit

Figure 2

State	D1	Startup	X1	D2	Clkout	C1
0	1	1	0	0	0	1
1	1	0	1	0	1	0
2	1	0	1	1	0	1
3	0	0	0	1	1	0
4	0	0	0	0	0	1
5	1	0	1	0	1	0

Figure 3.